



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE

United States Patent and Trademark Office

Address: COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

www.uspto.gov

|   |             |                      |                     |                  |
|---|-------------|----------------------|---------------------|------------------|
| APPLICATION NO.   | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
| 10/695,396  | 10/29/2003  | Kyong Seok Kim       | 041501-5582         | 6824             |
| 9629 7590 04/16/2008<br>MORGAN LEWIS & BOCKIUS LLP<br>1111 PENNSYLVANIA AVENUE NW<br>WASHINGTON, DC 20004 |             |                      |                     |                  |
| EXAMINER  |             |                      |                     |                  |
| CHUNG, DAVID Y  |             |                      |                     |                  |
| ART UNIT  |             | PAPER NUMBER         |                     |                  |
| 2871  |             |                      |                     |                  |
| MAIL DATE   |             | DELIVERY MODE        |                     |                  |
| 04/16/2008  |             | PAPER                |                     |                  |

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

# Office Action Summary

**Application No.**

10/695,396

**Applicant(s)**

KIM ET AL.

**Examiner**

DAVID Y. CHUNG

**Art Unit**

2871

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 11 January 2008.  
2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.  
3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-35 is/are pending in the application.  
4a) Of the above claim(s) 18-35 is/are withdrawn from consideration.  
5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.  
6) ☒ Claim(s) 1,6-8 and 14-17 is/are rejected.  
7) ☒ Claim(s) 2-5 and 9-13 is/are objected to.  
8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.  
10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☒ All b) ☐ Some \* c) ☐ None of:  
1. ☒ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)  
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3) ☐ Information Disclosure Statement(s) (PTO/SB-08)  
Paper No(s)/Mail Date \_\_\_\_\_  
4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date \_\_\_\_\_  
5) ☐ Notice of Informal Patent Application  
6) ☐ Other: \_\_\_\_\_

## DETAILED ACTION

### *Claim Rejections - 35 USC § 102*

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

- 1. Claims 1, 6-8 and 14-17 rejected under 35 U.S.C. 102(e) as being anticipated by Kumagawa et al. (US 6,909,415).**

As to claim 1, Kumagawa discloses an in-plane switching (IPS) mode liquid crystal display device in figures 19 and 20. Note in figure 20, the gate lines 1, data lines 2, thin film transistors 3, common electrodes 4, and pixel electrodes 5. The common electrode 4 comprises a storage line formed parallel to the gate lines. The thin film transistors are alternately positioned along lower and upper side pixel regions adjacent to corresponding gate lines. The pixel electrodes are formed integrally with the drain electrodes of the thin film transistors.

As to claim 6, because the drain electrode is formed integrally with pixel electrode 5, it is considered to overlap the storage line of common electrode 4, forming a storage capacitor.

As to claim 7, because the branches of the common electrode 4 are formed integrally with the storage line portion, they can be considered part of the storage line. Each pixel region comprises two branch portions of the common electrode formed near the edges and parallel to the adjacent data lines. These branch portions can be considered to be formed along a circumferential portion of the pixel region.

As to claim 8, the storage line portion of the common electrode extends parallel to the gate lines and is elongated to adjacent pixel regions by crossing under the data line along one side of the pixel region.

As to claims 14 and 15, the common electrode 4 in figure 20 appears to be formed in the same layer and of the same material as the gate line 1.

As to claims 16 and 17, the pixel electrode 5 is formed substantially in a center portion of the pixel electrode and is substantially parallel with the data line 2. The common electrode comprises branches which are formed near the edges of the pixel region and can be considered to be formed along a circumferential portion.

***Allowable Subject Matter***

Claims 2-5 and 9-13 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Y. Chung whose telephone number is (571) 272-2288. The examiner can normally be reached Monday thru Friday from 8:30 am to 5:00 pm. If successive attempts to contact the examiner are unsuccessful, the examiner's supervisor David C. Nelms can be reached at (571) 272-1787.

/David Y. Chung/

Examiner, Art Unit 2871

/David Nelms/

Supervisory Patent Examiner, Art Unit 2871